

SUBSTITUTE FORM PTO-1449
(MODIFIED)DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET N
06666/013001SERIAL NO.
08/986,327INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(use several sheets if necessary)

JUN 15 1998

(37 CFR 1.98)

PATENT AND TRADEMARK OFFICE

APPLICANT:
Svensson, et al.FILING DATE
12/5/97GROUP
2111

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		PATENT NUMBER							ISSUE DATE	PATENTEE		CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AB	AA	3	6	0	3	8	9	8	9/7/71	Dawson et al.				
AB	AB	5	0	9	5	2	2	3	3/10/92	Thomas				
AB	AC	5	3	3	9	2	3	6	8/16/94	Tamagawa				
	AD													
	AE													
	AF													
	AG													
	AH													
	AI													
	AJ													
	AK													

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

		DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL							
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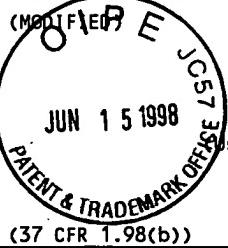
OTHER DOCUMENTS (including Author, Title, Date, Place of Publication)

AB	AQ	Seitz, et al., "Hot-Clock nMOS", Proceedings of the 1985 Chapel Hill Conference on VLSI, pp. 1-17, 1985
AB	AR	Koller, et al., "Adiabatic Switching, Low Energy Computing, and the Physics of Storing and Erasing Information", Proceedings of the Workshop on Physics and Computation (Physcomp '92), IEEE Press, pp. 1-5, 1993
AB	AS	Younis, et al., "Practical Implementation of Charge Recovering Asymptotically Zero Power CMOS", Proceedings on the 1993 Symposium on Integrated Systems, MIT Press, pp. 234-250, 1993

EXAMINER	A - BERHANE	DATE CONSIDERED	8/1/99
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EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

SUBSTITUTE FORM-PTO-1449

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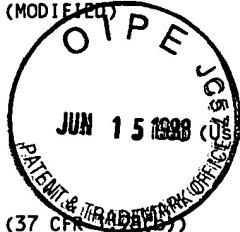
	AQ	Athas, et al., "An Energy-Efficient CMOS Line Driver Using Adiabatic Switching", <u>IEEE Great Lakes Symposium on VLSI</u> , 1994
	AR	Mead et al., "Introduction to VLSI Systems, Chapter 1, MOS Devices and Circuits", Addison-Wesley, pp. 1-37, 1980
	AS	Weste et al., "Principles of CMOS VLSI Design, a Systems Perspective, Chapter 5, CMOS Circuit and Logic Design", Addison-Wesley, Second Edition, pp. 160-231, 1993

EXAMINER A-BERHANE DATE CONSIDERED 8/7/97

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OTHER DOCUMENTS (including Author, Title, Date, Place of Publication)

AB	AQ	Nordin, et al., "A systems Perspective on Digital Interconnection Technology", <u>IEEE Journal of Lightwave Technology</u> , Vol 10, No. 6, pp. 811-827, June 1992
AB	AR	Slater, "MIPS Previews 64-Bit R4000 Architecture", <u>Microprocessor Report</u> , Vol. 5, No. 2, February 6, 1991
AB	AS	Svensson et al., "Adiabatic Charging Without Inductors", <u>USC/ISI Technical Report ACMOS-TR-3</u> , December 17, 1993

EXAMINER A. BERHANEDATE CONSIDERED 8/99

EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

SUBSTITUTE FORM PTO-1449 (MODIFIED)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 06666/013001	SERIAL NO. 08/986,327
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) JUN 15 1998 JC5 (37 CFR 1.98(b))		APPLICANT: Svensson et al.			
		FILING DATE 12/5/97	GROUP 2111		

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA						
AB						RECEIVED
AC						JUN 20 1998
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AE						GROUP 2100
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AG						
AH						
AI						
AJ						
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FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

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						YES	NO
AL							
AM							
AN							
AO							
AP							

OTHER DOCUMENTS (including Author, Title, Date, Place of Publication)

<i>AB</i>	AQ	Athas et al., "An Energy-Efficient CMOS Line Driver Using Adiabatic Switching", <u>USC/ISI Technical Report ACMOS-TR-2a</u> , November 3, 1993
<i>AB</i>	AR	Chandrasekaran, et al., "Low-Power CMOS Digital Design", <u>IEEE Journal of Solid-State Circuits</u> , Vol. 27, No. 4, pp. 473-484, April 1992
<i>AB</i>	AS	Burr, et al., "Energy Considerations in Multichip-Module based Multiprocessors", <u>IEEE International Conference on Computer Design</u> , pp. 593-600, 1991

EXAMINER A. BERHANE DATE CONSIDERED 8/99

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INFORMATION DISCLOSURE CITATION
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Atty. Docket No. 07067.0008		Serial No. 08/986,327	
Applicant Lars SVENSSON et al.			
Filing Date December 5, 1997	Group 2838		
U.S. PATENT DOCUMENTS			
Examiner Initial*	Document Number	Date	Name
B	5,459,414	10/17/95	Dickinson
	5,473,269	12/05/95	Dickinson
	5,506,520	04/09/96	Frank et al.
	5,517,145	05/14/96	Frank
	5,521,538	05/28/96	Dickinson
	5,526,319	06/11/96	Dennard et al.
	5,602,497	02/11/97	Thomas
	5,604,454	02/18/97	Maguire et al.
	5,675,263	10/07/97	Gabara
C	5,838,203	11/17/98	Stamoulis et al.
RECEIVED 1999 JUN 29 PM 4:16 MOLONY CENTER 12800			
FOREIGN PATENT DOCUMENTS			
	Document Number	Date	Country
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
	Mateo, D., et al. "Quasi-adiabatic ternary CMOS logic", Electronics Letters, Vol. 32, No. 2, 1/18/96.	COPY NOT PROVIDED (CNP)	
	Younis, Saed, et al., "Non-Dissipative Rail Drivers for Adiabatic Circuits", IEEE, p.404-414, 1995	(CNP)	
	Dickinson, Alex, et al. "Adiabatic Dynamic Logic", IEEE, p.282-285, 1994	(CNP)	
	Ye, Yibin, et al. "Energy Recovery Circuits Using Reversible and Partially Reversible Logic", IEEE p.769-778, 1996.	COPY NOT PROVIDED	
Examiner	A - BERHANE	Date Considered	8/99
*Examiner:	Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce		

INFORMATION DISCLOSURE CITATION
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Atty. Docket No.	07067.0008	Serial No.	08/986,327
Applicant	Lars SVENSSON et al.		
Filing Date	December 5, 1997	Group	2838

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Examiner Initial*	Document Number	Date	Name	Class	Sub Class	Filing Date If Appropriate
A3	5,841,299	11/24/98	De			
A3	5,870,331	02/09/99	Hwang et al.			
A3	5,880,602	03/09/99	Kaminaga et al.			
A3	5,889,439	03/30/99	Meyer et al.			
A3	5,881,014	03/09/99	Ooishi			
A3	5,883,538	03/16/99	Keeth et al.			

FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner A. BERHANE Date Considered 8/99

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